

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	1	"6845410"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/26 15:47
L5	30	"709"/\$.ccls. and (RS232 or (RS adj "232")) and UART and transmission and control and "I/O" and PCI	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/26 15:48
L6	30	"709"/\$.ccls. and (RS232 or (RS adj "232")) and UART and transmission and control and "I/O" and PCI	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/26 15:49
S1	2	("6356130" or "6574751")	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 09:11
S2	1	"20030105875"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 09:10
S3	2	"709"/\$.ccls. and (RS232 or (RS adj "232")) and UART and SOC	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 13:23
S4	33	(RS232 or (RS adj "232")) and UART and SOC and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 09:33
S5	8	(RS232 or (RS adj "232")) and UART and SOC and @ad<"20011204" and server and bus and FIFO	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 09:35
S6	0	RS232 and (UART with FIFO) and SOC and @ad<"20011204" and server and bus	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 16:45
S7	120	"709"/\$.ccls. and (RS232 or (RS adj "232")) and UART and transmission and control and "I/O"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 13:25
S8	30	"709"/\$.ccls. and (RS232 or (RS adj "232")) and UART and transmission and control and "I/O" and PCI	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/26 15:48
S9	7	S8 and SDRAM	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 13:27
S10	8	RS232 and UART and SOC and @ad<"20011204" and server and bus and FIFO	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 13:53
S11	25	(RS232 or (RS adj "232")) and UART and ( transmission or trans\$6) and control and "I/O" and PCI and SDRAM and chip	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 13:59

S12	10	(RS232 or (RS adj "232")) and UART and ( transmission or trans\$6) and control and "I/O" and PCI and SDRAM and chip and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 14:03
S13	4	(RS232 or (RS adj "232")) and UART and ( transmission or trans\$6) and control and "I/O" and PCI and server and SDRAM and chip and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:07
S14	0	(RS232 or (RS adj "232")) and PCI and server and SDRAM and I2C and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:08
S15	7	(RS232 or (RS adj "232")) and PCI and server and I2C and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:09
S16	7	(RS232 or (RS adj "232")) and PCI and server and I2C and trans\$6 and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:17
S17	16	(RS232 or (RS adj "232")) and PCI and (super near "i/o") and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:31
S18	1	(RS232 or (RS adj "232")) and PCI and (super near "i/o") and (I2C or (inter with integrat\$3)) and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:32
S19	33	RS232 and (UART with FIFO) and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/26 15:47
S20	0	RS232 and (UART with FIFO) and @ad<"20011204" and (super near "i/o")	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 16:48
S21	0	RS232 and (UART with FIFO) and @ad<"20011204" and super	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 16:48
S22	8	(UART with FIFO) and @ad<"20011204" and super	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 16:49

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**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**16 The design of a 1.75-D 1280-channel ultrasound imaging system***Puyun Guo; Shikui Yan; Qing Zhu;*

Bioengineering Conference, 2001. Proceedings of the IEEE 27th Annual Northeast , 31 March-1 April 2001

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**19 Augmented reality visualization of ultrasound images: system description, calibration, and features***Sauer, F.; Khamene, A.; Bascle, B.; Schinunang, L.; Wenzel, F.; Vogt, S.;*

Augmented Reality, 2001. Proceedings. IEEE and ACM International Symposium on , 29-30 Oct. 2001

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**26 A high frequency intravascular ultrasound imaging system for investigation of vessel wall properties**

*Ryan, L.K.; Lockwood, G.R.; Starkoski, B.G.; Holdsworth, D.W.; Rickey, D.W., Drangova, M.; Fenster, A.; Foster, F.S.;*  
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**27 Applications of microelectronics and microfabrication to ultrasound imaging systems**

*Robinson, A.L.; Mo, J.-H.;*  
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*Freear, S.; Hoyle, B.S.; Bailey, N.J.;*  
Multiprocessor DSP (Digital Signal Processing) - Applications, Algorithms and Architectures, IEE Colloquium on (Digest No.1995/116) , 31 May 1995  
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**29 Detection Performance Theory for Ultrasound Imaging Systems**

*Zemp\* R. J. ; Parry M. D. ; Abbey C. K. ; Insana M. F. ;*  
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